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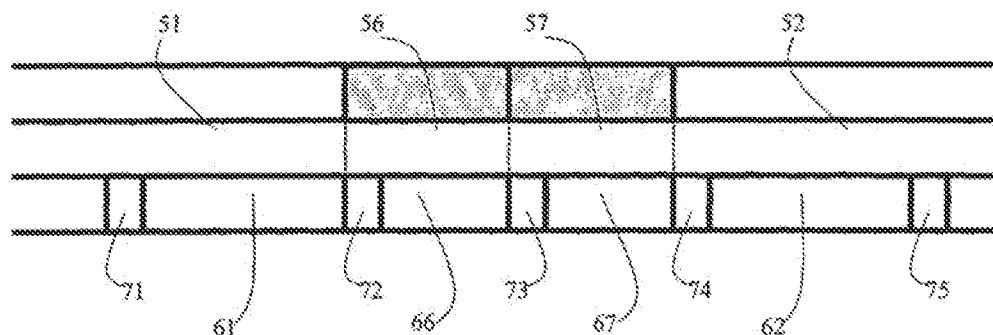
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(54) Title: **RECORD CARRIER COMPRISING AN ADDITIONAL SYNC-COLOR PATTERN AND METHOD AND DEVICE FOR USE WITH SUCH RECORD CARRIER**



(57) Abstract: The invention relates to a format for storing information on an optical recording medium. According to said format the record carrier comprises a synchronization pattern, which synchronization pattern comprises an identifying part consisting of a bit sequence chosen from 100 101, 010 101, 101 001, 010 100 or 100 100.

WO 2004/042720 A2

Record carrier comprising an additional sync-color pattern and method and device for use with such record carrier

The invention relates to a record carrier comprising synchronization patterns for identifying blocks of information, said synchronization patterns comprising a part for distinguishing one such synchronization pattern from another such synchronization pattern. The invention especially relates to a record carrier according to the Blu-ray Disc format. The invention further relates to a methods for recording information on said record carrier and for
5 retrieving information from said record carrier, and to devices for recording information on said record carrier and for retrieving information from said record carrier.

In the current Blu-ray Disc format according to the Blu-ray Disc standard
10 (Blu-ray Disc System Description, Rewritable Format, part 1, Basic Format Specifications) 7 synchronization patterns, FS0 to FS6, are defined, each synchronization patterns having its own sync-color pattern. In this context a sync-color pattern is meant to be a unique part of a synchronization pattern, which part distinguishes one such synchronization pattern from another such synchronization pattern. A sync-color pattern may be considered to be a
15 signature identifying otherwise identical synchronization patterns.

Synchronization patterns are used, for example, to signify the start of an information block stored on the record carrier, such as a Recording Frame according to the Blu-ray Disc format. By applying a structure of distinguishable synchronization patterns it is possible to identify distinct information blocks.

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According to the Blu-ray Disc standard a sync-color pattern consist of 6 information bits. These 6 bit sync-color patterns comply with the constraints of the 17PP modulation code (although the synchronization pattern themselves do not comply); an RLL-
25 code with $d=1$ and $k=7$, and a Repeated Minimum Transition Runlength (RMTR) of 6. Moreover, for integrity reasons, these patterns are chosen such that their distance with relation to transition shifts (that is, their Hamming distance when only transition shifts occur) is greater than or equal to 2. The 17PP modulation code itself, and devices for recording and

retrieving modulated data, is described in International Application WO 99/63671, while sync-color patterns are described in International Application WO 99/22375.

For certain applications it is advantageous to have at least an additional 8th sync-color pattern. For example, synchronization patterns comprising such an additional sync-color could be required in an extension of the rewritable Blu-ray Disc standard to a ROM (Read Only Memory) version of this standard. These additional sync-color patterns would preferably also comply with the above mentioned constraints. However, providing such an additional sync-color pattern appears to be difficult.

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It is an object of the present invention to provide a record carrier comprising such additional sync-color patterns, in addition to the existing 7 sync-color patterns, which comply as good as possible with the above-mentioned constraints.

This object is achieved according to a first aspect of the invention when the additional sync-color pattern is chosen to be a pattern consisting of either the 6 information bits 100 101 or of the 6 information bits 010 101. Such an additional sync-color pattern is, for example, referred to as FS7.

Although these sync-color patterns themselves comply with the modulation code constraints and Hamming distance constraints, they may under some conditions generate a RMTR violation when followed by specific source data bits. A worst-case scenario where a Repeated Minimum Transition Runlength (RMTR) of 7 occurs is shown in Figure 3. Two embodiments of the invention overcoming such possible artifacts are described below.

According to a first embodiment of the invention the source data bits sequence 01 11 01 11 directly behind an additional sync-color pattern consisting of either the 6 information bits 100 101 or of the 6 information bits 010 101 is excluded. This will add a small constraint on the source data. However, such a constraint can easily be incorporated in the format, for example when the bits of the sync-color pattern are followed by bits representing an address. When, for example, the first bit following the sync-color pattern bits is fixed to a 1, the constraint is complied with.

According to a second embodiment of the invention the last nibble (that is, the last three bits) of the additional sync-color pattern consisting of either the 6 information bits 100 101 or of the 6 information bits 010 101 is treated as representing data bits 11 encoded to the three bits modulation word, which is according to the 17PP modulation code as is shown

in the conversion table in Figure 1. Subsequently the RMTR substitution according to the 17PP modulation coding rules is done on the data bits only. Now, the channel decoder can detect the additional sync-color pattern, and perform the inverse substitution unambiguously. An example of this is shown in Figure 4.

5 The object of the invention is achieved according to a second aspect of the invention when the additional sync-color pattern is chosen to be a pattern consisting of either the 6 information bits **101 001**, the 6 information bits **010 100**, or of the 6 information bits **100 100**. When the constraint of the Hamming distance being greater than or equal to 2 is released, several possible sync-color patterns become available.

10 In the table shown in Figure 5 the existing sync-colors patterns FS0 - FS6 are ordered in a systematic way in the left part of the table. The right part of the table shows alternative additional sync-colors patterns A - K. The neighboring sync-colors patterns having a transition shift distance of 1 are shown between brackets. Sync-color patterns **101 001** (A), **010 100** (G), and **100 100** (H) have a transition shift distance of 1 to only one
15 existing sync-color pattern. These patterns are therefore preferred as additional sync-color patterns. An further advantage of these patterns (A, G, H) is the lack of a possible RMTR violation.

20 These objects, features and advantages of the invention are described with reference to the accompanying drawings in which

Figure 1 shows the 17PP modulation code conversion table,

Figure 2 shows the synchronization patterns comprising the known sync-color patterns,

25 Figure 3 shows a worst-case scenario where a Repeated Minimum Transition Runlength of 7 occurs,

Figure 4 shows an example of an embodiment according to a first aspect of the invention,

Figure 5 shows a table with the existing sync-colors patterns and the
30 alternative additional sync-colors patterns according to a second aspect of the invention, and

Figure 6 shows an example of a data bit stream comprising synchronization patterns, as stored on a record carrier.

Figure 1 shows the 17PP modulation code conversion table. The data bits are converted to modulation bits according to this table. The modulation bit stream is stored on the record carrier, where a 1 in the modulation bit stream causes a transition in the signal stored on the record carrier. This modulation code is a Run Length Limited (RLL) code with $d=1$ and $k=7$, resulting in runlengths (that is, the number of consecutive 0's or 1's in the data bit stream encoded on the record carrier) ranging from a minimum runlength of 2 to a maximum runlength of 8. Moreover, this 17PP modulation code has the constraint that the Repeated Minimum Transition Runlength (RMTR) is equal to 6, causing the number of consecutive minimum runlengths to be limited to 6.

Figure 2 shows seven known synchronization patterns. These synchronization patterns are numbered FS0 to FS6. Each synchronization pattern consists of a 24 bit sync body followed by a 6 bit sync-color pattern. The sync body is identical for all synchronization patterns, and signals the presence of a synchronization pattern. The sync-color pattern is a signature identifying a specific synchronization pattern. These specific synchronization patterns can be used to mark specific locations in a data bit stream, such as for example the start of an information block.

For integrity reasons (that is robustness against read errors, these sync-color patterns are chosen such that their distance with relation to transition shifts (that is, their Hamming distance when only transition shifts occur) is greater than or equal to 2. It is assumed that due to read errors only transition shift occur, and that no transitions are added or removed.

Figure 6 shows a data bit stream as stored on a record carrier. The data bit stream comprises two information blocks 51, 52 that are linked by two link frames 56, 57. The information blocks consist of parts holding the information itself 61, 62 and of synchronization patterns 71, 74, 75. Such synchronization patterns may be chosen from the synchronization patterns FS0-FS6 shown in Figure 2. It is preferred to have a specific synchronization pattern 74 reserved for indicating the start of an information block. Such a specific synchronization pattern 74 is, for example, synchronization pattern FS0 identified by its sync-color pattern 000 001. The other synchronization patterns 71, 75 (chosen from FS1-FS6) are used to mark specific locations within the information blocks. Two link frames 56, 57 link the two succeeding information blocks 51, 52. Each link frame consists of a synchronization pattern 72, 73 and a link frame body 66, 67. To distinguish the synchronization patterns 72, 73 in the link frames from the synchronization patterns 71, 74, 75 in the information blocks, these synchronization patterns 72, 73 in the link frames

comprise an additional sync-color pattern according to the invention, such as for example a pattern consisting of the 6 information bits 100 101. The link frame bodies 66, 67 may consist of any data bits as long as the first 8 bits directly behind the additional sync-color pattern is not the data bits sequence 01 11 01 11, as this would cause a violation of the constraints. It is noted that, in an alternative embodiment, the synchronization pattern 73 in the second link frame 57 may be chosen from the synchronization patterns PS0-PS6 shown in Figure 2, because the sequence of two link frames in the data bit stream is already signaled by the synchronization pattern 72 in the first link frame 56 comprising the distinguishing additional sync-color pattern.

CLAIMS:

1. A record carrier comprising synchronization patterns for identifying blocks of information, said synchronization patterns comprising a part for distinguishing one such synchronization pattern from another such synchronization pattern, characterized in that the part for distinguishing the synchronization patterns consists of the bit sequence **100 101** or of
5 the bit sequence **010 101**.
2. Record carrier according to claim 1, characterized in that the synchronization pattern comprising a part for distinguishing the synchronization patterns consisting of the bit sequence **100 101** or of the bit sequence **010 101**, is directly followed by a bit sequence not
10 violating the Repeated Minimum Transition Runlength constraint.
3. Record carrier according to claim 2, characterized in that the synchronization pattern comprising a part for distinguishing the synchronization patterns consisting of the bit sequence **100 101** or of the bit sequence **010 101**, is directly followed by any 8 bit data bit
15 sequence except the sequence **01 11 01 11**.
4. A record carrier comprising synchronization patterns for identifying blocks of information, said synchronization patterns comprising a part for distinguishing one such synchronization pattern from another such synchronization pattern, characterized in that the
20 part for distinguishing the synchronization patterns consists of the bit sequence **101 001**, or the bit sequence **010 100**, or the bit sequence **100 100**.
5. A device for recording synchronization patterns for identifying blocks of information onto a record carrier, said synchronization patterns comprising a part for
25 distinguishing one such synchronization pattern from another such synchronization pattern, characterized in that the device is operative for recording synchronization patterns comprising a part for distinguishing the synchronization patterns which consists of the bit sequence **100 101** or of the bit sequence **010 101**.

6. A device for recording synchronization patterns for identifying blocks of information onto a record carrier, said synchronization patterns comprising a part for distinguishing one such synchronization pattern from another such synchronization pattern, characterized in that the device is operative for recording synchronization patterns
5 comprising a part for distinguishing the synchronization patterns which consists of the bit sequence **101 001**, or the bit sequence **010 100**, or the bit sequence **100 100**.

7. A device for retrieving data patterns from a record carrier, said data patterns comprising synchronization patterns for identifying blocks of information, said
10 synchronization patterns comprising a part for distinguishing one such synchronization pattern from another such synchronization pattern, characterized in that the device is operative for identifying a synchronization pattern comprising a part for distinguishing the synchronization patterns which consists of the bit sequence **100 101** or of the bit sequence **010 101**.

15 8. A device for retrieving data patterns from a record carrier, said data patterns comprising synchronization patterns for identifying blocks of information, said synchronization patterns comprising a part for distinguishing one such synchronization pattern from another such synchronization pattern, characterized in that the device is
20 operative for identifying a synchronization pattern comprising a part for distinguishing the synchronization patterns which consists of the bit sequence **101 001**, or the bit sequence **010 100**, or the bit sequence **100 100**.

1/3

Data bits	Modulation bits	
00 00 00 00	010 100 100 100	
00 00 10 00	000 100 100 100	
00 00 00	010 100 000	
00 00 01	010 100 100	
00 00 10	000 100 000	
00 00 11	000 100 100	
00 01	000 100	
00 10	010 000	
00 11	010 100	
01	010	
10	001	
11	000	If preceding modulation bits = xx1
	101	If preceding modulation bits = xx1

Data bit pattern to be substituted	Substituting modulation bits	Condition for substitution
11 01 11	001 000 000	If next modulation bits = 010

Terminating data bits	Terminating modulation bits	
00 00	010 100	
00	000	

Fig. 1

Sync number	24-bit sync body	6-bit sync-colour pattern
FS0	#01 010 000 000 010 000 000 010	000 001
FS1	#01 010 000 000 010 000 000 010	010 010
FS2	#01 010 000 000 010 000 000 010	101 000
FS3	#01 010 000 000 010 000 000 010	100 001
FS4	#01 010 000 000 010 000 000 010	000 100
FS5	#01 010 000 000 010 000 000 010	001 001
FS6	#01 010 000 000 010 000 000 010	010 000

Fig. 2

2/3

	Sync body	FS7	data
Data bits			01 11 01 11
Modulation bits	#01 010 000 000 010 000 000 010	100 101	010 101 010 101 000

Fig. 3

	Sync body	FS7	data
Data bits		11	01 11 01 11
Modulation bits	#01 010 000 000 010 000 000 010	100 101	010 101 010 101 000
Substituted modulation bits			000 000

Fig. 4

3/3

Existing sync colours			Alternatives		
FS0	000 001	(F)	A	101 001	(FS2, I)
FS3	100 001	(B, D)	B	010 001	(FS1, FS3, FS5, FS6)
FS5	001 001	(B, C, E, K)			
			C	001 000	(FS4, FS5, FS6)
FS2	101 000	(A, H)			
FS6	010 000	(B, C)	D	100 010	(FS1, FS3, H)
			E	001 010	(FS1, FS5)
FS1	010 010	(B, D, E, G)	F	000 010	(FS0, FS4)
FS4	000 100	(C, F, K)	G	010 100	(FS1, H, I)
			H	100 100	(FS2, G, I)
			I	100 101	(A, H, J)
			J	010 101	(G, I)
			K	000 101	(FS5, FS4)

Fig. 5

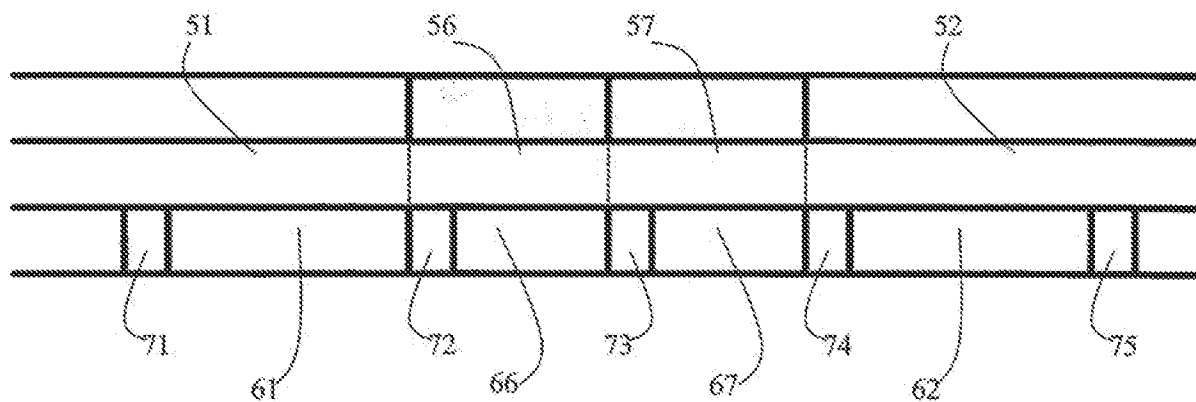


Fig. 6